

1/8

FIG 1

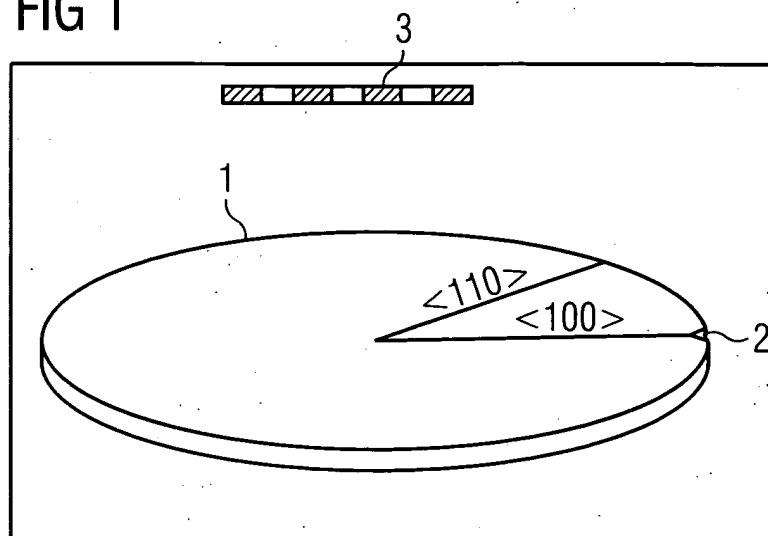


FIG 2

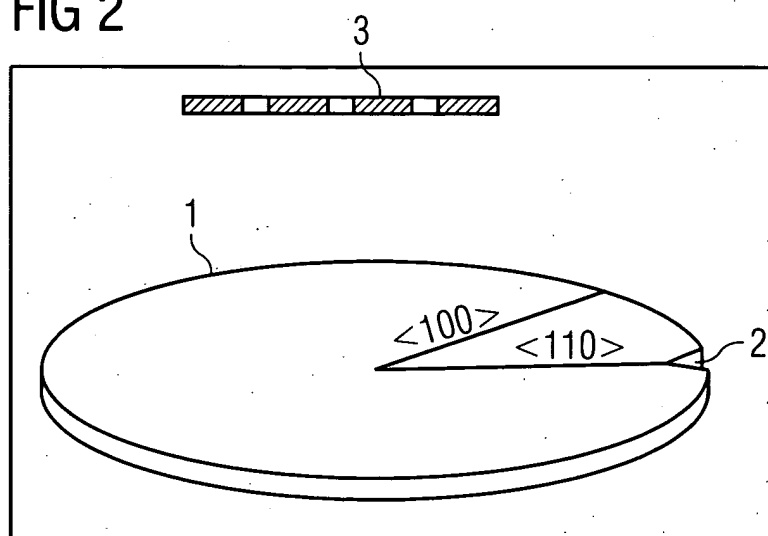


FIG 3

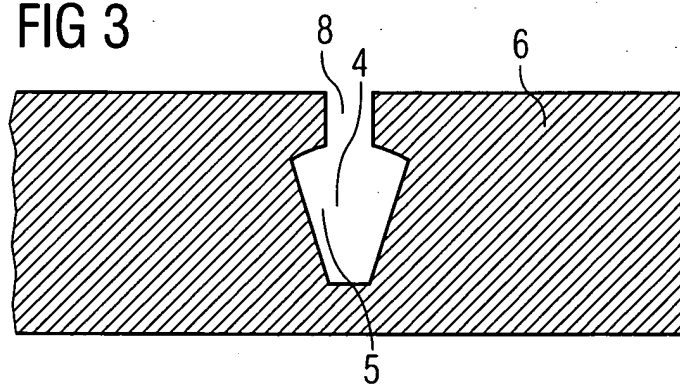


FIG 4A

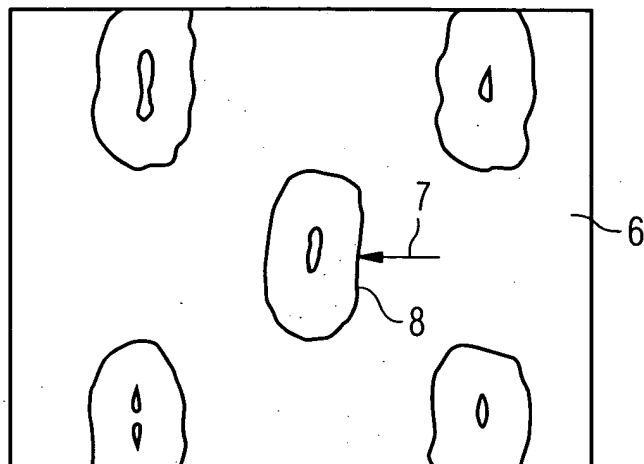


FIG 4B

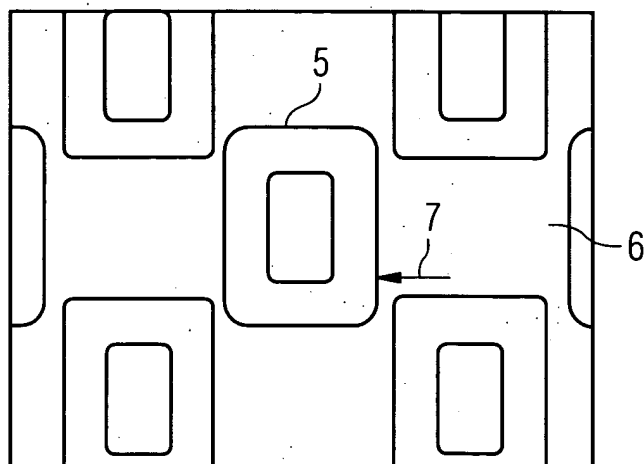


FIG 4C

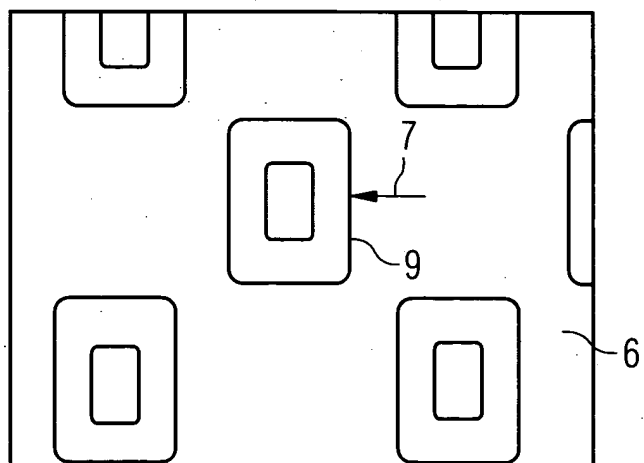


FIG 5A

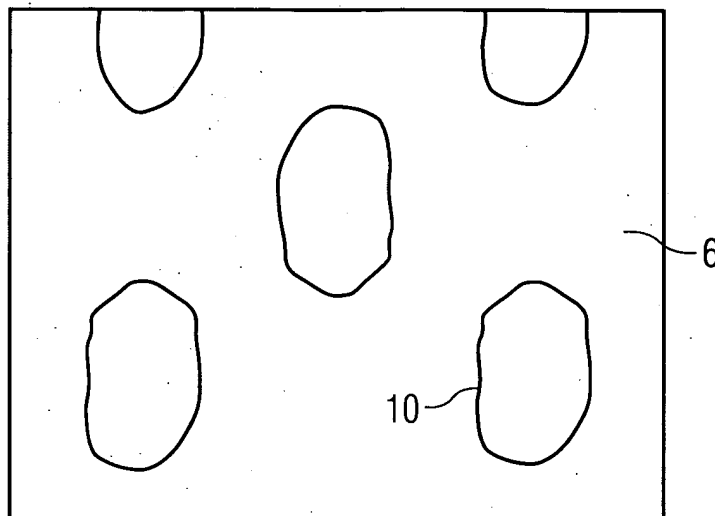


FIG 5B

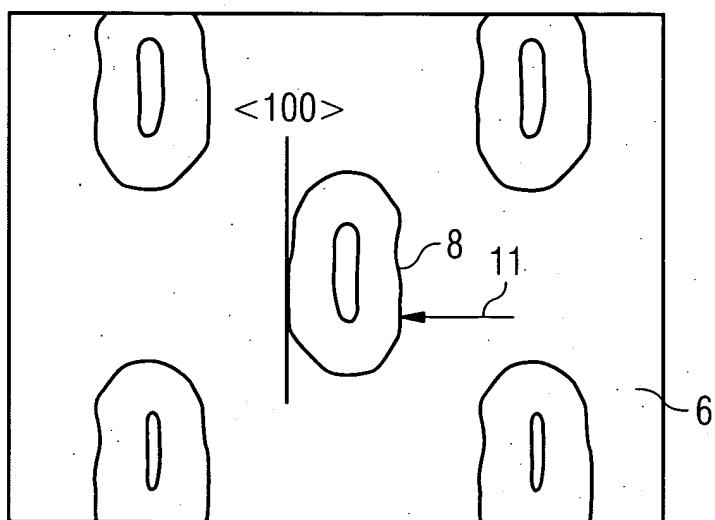


FIG 5C

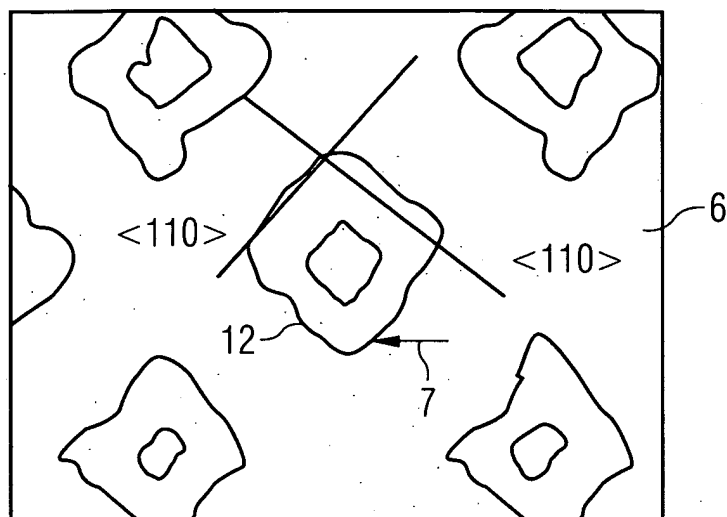


FIG 5D

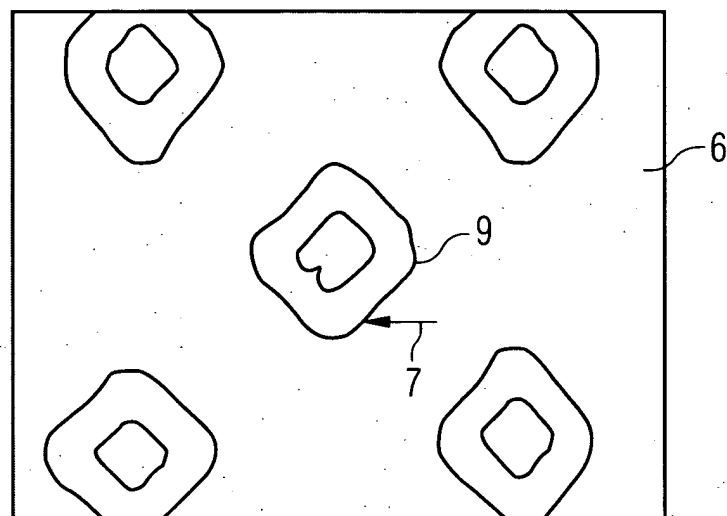


FIG 6A

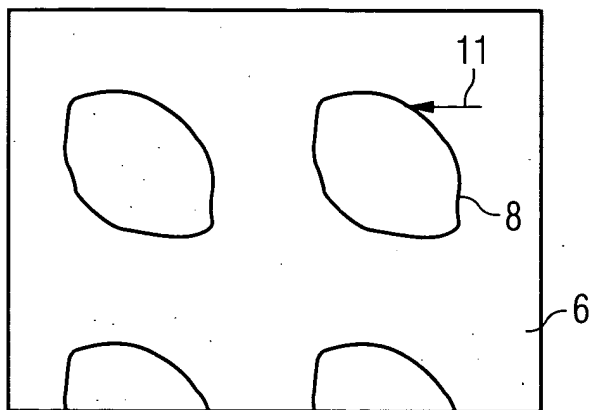


FIG 6B

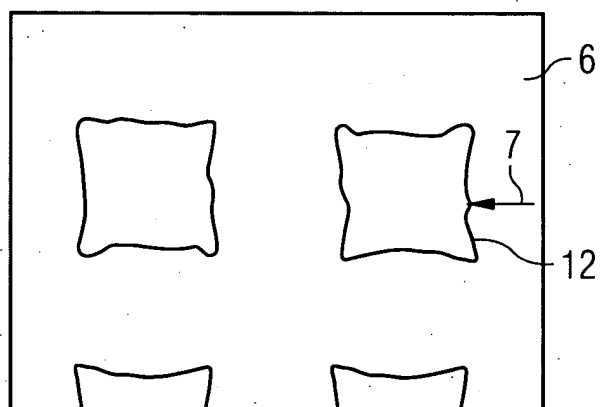


FIG 6C

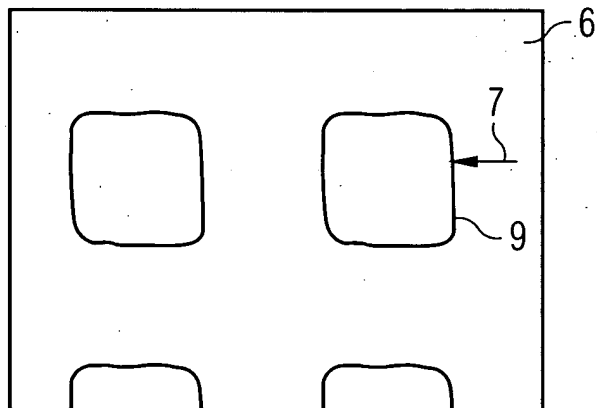


FIG 6D

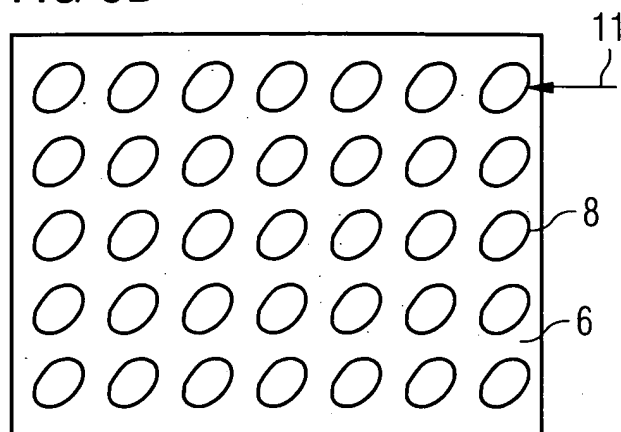


FIG 6E

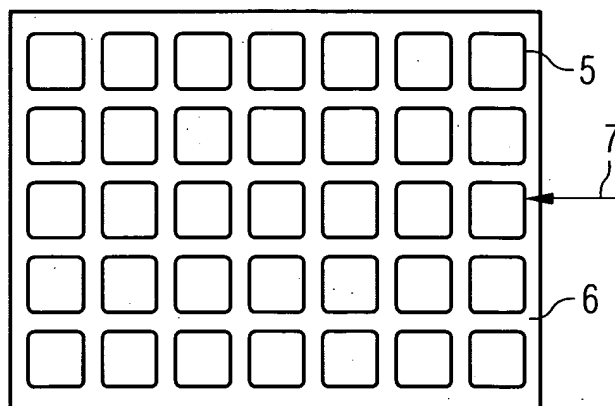


FIG 6F

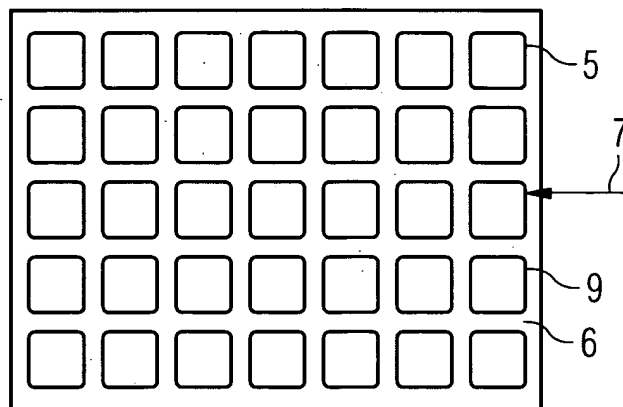


FIG 7A

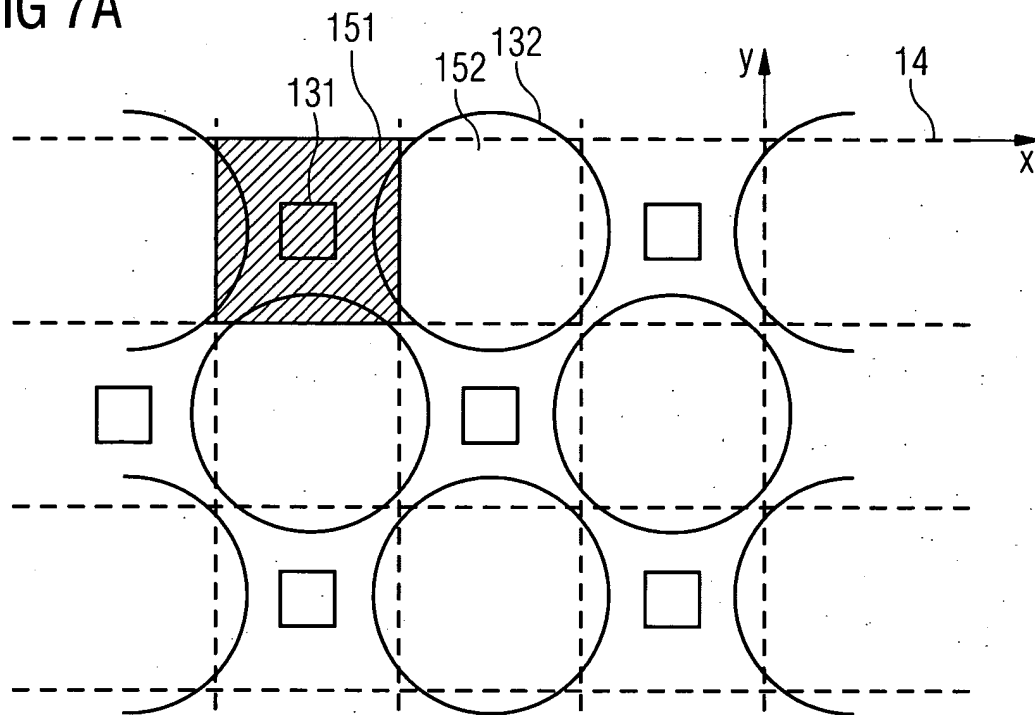


FIG 7B

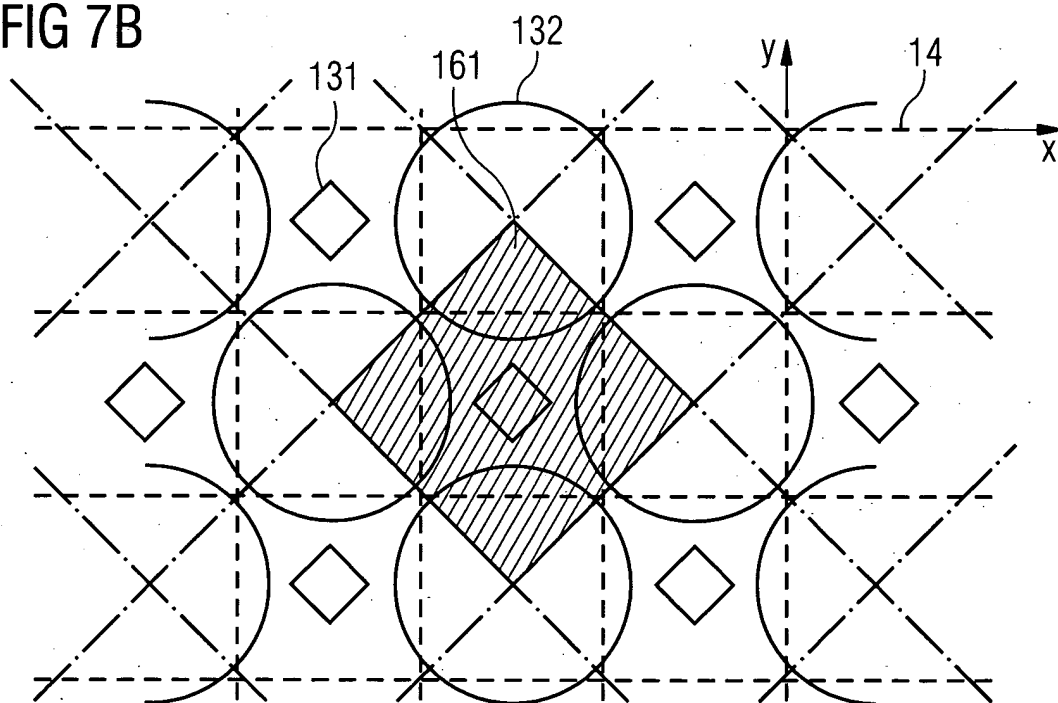
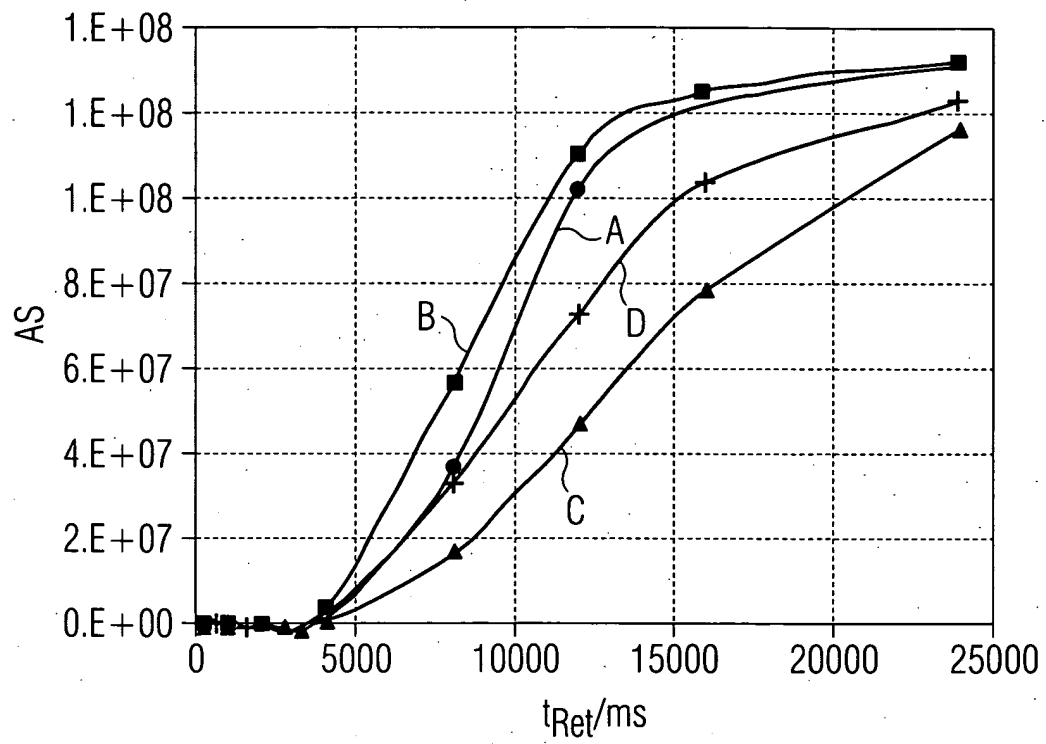


FIG 8



- A: nicht rotierter Wafer
- B: nicht rotierter Wafer -10% Speicherkapazität
- ▲— C: rotierter Wafer
- +— D: rotierter Wafer -10% Speicherkapazität